

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10042604	01/09/2002	365	149	2818	AUDUONG

**APPLICANTS: Hsu Louis; Joshi Rajiv;

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO YOR9-2001-0578(728-225)
TITLE : Semiconductor memory system having dynamically delayed timing for high-speed data transfers			
U.S.DEP.T. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claims for O.G.
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Fig.s Drwg.	Print Fig.
TERMINAL DISCLAIMER		Primary Examiner		
		Application Examiner		
PREPARED FOR ISSUE				
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